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UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Kenneth Shepard

Examiner Ayal I. Sharon

Serial No.

09/591,270

Art Unit

2123

Filed

June 9, 2000

For

METHODS FOR ESTIMATING THE BODY VOLTAGE OF

DIGITAL PARTIALLY DEPLETED SILICON-ON-

INSULATOR CIRCUITS

RESPONSE TO REQUIREMENT TO SUBMIT INFORMATION UNDER 37 C.F.R. § 1.105

RECEIVED I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: JUL 0 7 2004

Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450

Technology Center 2100

June 24, 2004

Date of Deposit

Paul Ackerman Attorney Name

39,891

PTO Registration No

Signature

June 24, 2004

Date of Signature

Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450

Dear Sir:

Applicants respectfully petition for a two-month extension of time to respond to the Office Action mailed on February 24, 2004. A check is enclosed to cover the extension fee.

The following remarks and Exhibits 1 and 2 are submitted in response to the

Examiner's requirement, pursuant to 37 C.F.R. § 1.105, that Applicants submit additional 06/30/2004 AADOFO1 00000097 09591270

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information concerning the declaration by the inventor, Dr. Kenneth Shepard, that was filed on December 8, 2003, pursuant to 37 C.F.R. § 1.131.